HP 13255

ASYNCHRONOUS MULTIPOINT INTERFACE MODULE

Manual Part No. 13255-91106

PRINTED

AUG-01-76

DATA TERMINAL TECHNICAL INFORMATION





1.0 INTRODUCTION.

The Asynchronous Multipoint Interface Module provides an RS232C compatible, asynchronous data communications interface. Hardware is provided (non RS232C) to permit multipoint daisy-chaining of terminals. A programmable baud rate generator is included as well as two bytes of program accessable switches. Refer to module section 13255-91086 for parts lists for the US Modem Cable Assembly (02640-60131) and the Data Comm Self Test Hood Assembly (02645-60002).

2.0 OPERATING PARAMETERS.

A summary of operating parameters for the Asynchronous Multipoint Interface Module is contained in tables 1.0 through 6.7.

Size $(L \times W \times D)$ Part Weight I +/-0.100 Inches Number Nomenclature (Pounds) I 5061-2403 | Modem Bypass Cable Ass'y N/A N/A 5061-2409 I European Modem Cable Ass'y N/A N/A 02640-60106 | Async Multipoint I/F PCA $12.9 \times 4.0 \times 0.6$ 0.50 02640-60131 | US Modem Cable Assembly N/A N/A 02640-60132 | Modem Multipoint Cable Ass'y N/A N/A 02640-60133 | Multipoint Cable Ass'y N/A N/A 02640-60134 | Multipoint Extension N/A N/A Cable Assembly 02640-60140 | Power Down Protect PCA N/A N/A 02645-60002 | Data Comm Self Test Hood N/A N/A 02645-60004 | Self Test Connector Ass'y N/A N/A Number of Backplane Slots Required: 1

Table 1.0 Physical Parameters

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NOTE: This document is part of the 264XX DATA TERMINAL product series Technical Information Package (HP 13255).

1.0 INTRODUCTION.

The Asynchronous Multipoint Interface Module provides an RS232C compatible, asynchronous data communications interface. Hardware is provided (non RS232C) to permit multipoint daisy-chaining of terminals. A programmable baud rate generator is included as well as two bytes of program accessable switches. Refer to module section 13255-91086 for parts lists for the US Modem Cable Assembly (02640-60131) and the Data Comm Self Test Hood Assembly (02645-60002).

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Table 1.0 Physical Parameters

Table 2.0 Reliability and Environmental Information

=	Environmental:	(x)	HP (Class	В	() Ot	her:		22223		222
1 1 1 1 1 1	Restrictions:	Type	teste	ed at	p ro d	luct	level						
1 = 	F	ailure	Rate	===== e:	1.876		(percent	per	1000	hours)		22222	

Table 3.0 Power Supply and Clock Requirements - Measured (At +/-5% Unless Otherwise Specified)

22222222222222222						
+5 Volt Supply	+12 Volt Supply	-12 Volt Supply	+42 Volt Supply			
@ 400 mA	@ 150 mA	@ 70 mA	Am 9			
		(An additional 200 mA required for 02640-60140				
1		cable)	NOT APPLICABLE			
	3033332333333	====================================				
115 volt	s ac	220 vol	ts ac			
	A	• • • • • • • • • • • • • • • • • • •	A			
NOT APPLI	CABLE	NOT APPL	ICABLE			
	************	=======================================				
	Clock Frequency: 4.915 MHz +/-0.1%					

Table 4.0 Jumper Definitions

PCA	Function				
Designation	In	Out			
INT	Interrupt on ATN2	Interrupt on ATN			
	Respond to an Interrupt poll on any one of these bus data bits	No Effect			
A4,A11,A10,A9	Address Bit is a "0" in the Module Address	 			
-12	Connect -12V to P2, Pin N	No Effect			
		Transmit and receive char- acters framed with 1 start and 2 stop bits			

Table 5.0 Connector Information

Table 5.0 Connector Information					
	Signal	Signal			
<pre>Connector and Pin No.</pre>	-	Description			
i and Pin No.	name	hezetibeton i			
P1, Pin 1	+5V	LE Valt Dawas Cupalit			
1 21, 2111 1	1 754	+5 Volt Power Supply			
-2	GND	Ground Common Return (Power and Signal)			
-3	SYS CLK	I 4.915 MHz System Clock			
-4	 -12V	-12 Volt Power Supply			
-5	ADDRO				
 -6	ADDR1				
-7	1	i Not Used i			
-8	ADDR3	Negative True, Address Bit 3			
 -9	ADDR4				
-10	ADDR5				
-11	ADDR6	Negative True, Address Bit 6			
-12	ADDR7				
-13	ADDR8				
-14	ADDR9	Negative True, Address Bit 9			
-15	ADDR10	Negative True, Address Bit 10			
-16	ADDR11	Negative True, Address Bit 11			
-17	1	 } 			
-18	T 1				
-19	1	} Not Used			
-20	† •				
-21	1/0	Negative True, Input Output/Memory			
-22	GND	Ground Common Return (Power and Signal)			

Table 5.0 Connector Information (Cont'd.)

	=======================================	
<pre>Connector</pre>	Signal	Signal
and Pin No.	i Name	Description (
======================================	======================================	
P1, Pin A	I GND	Ground Common Return (Power and Signal)
-B	POLL	Negative True, Polled Interrupt
1	i POBL	I Identification Request
i	1	l localization request
i -c	+12V	+12 Volt Power Supply
İ	1	
-D	PWR ON	System Power On
j	·	l I
-E	BUS0	Negative True, Data Bus Bit 0
!		
-F	BUS1	Negative True, Data Bus Bit 1
1 - H	BUS2	Negative True, Data Bus Bit 2
-n	1 5032	heyacive live, baca ous bit 2
- J	BUS3	Negative True, Data Bus Bit 3
i		
-K	BUS4	Negative True, Data Bus Bit 4
1	i	1
-L	BUS5	Negative True, Data Bus Bit 5
!		Nametalus Maura Data Dua Data (
-M	BUS6	Negative True, Data Bus Bit 6
-N	BUS7	Negative True, Data Bus Bit 7
,		l Regultive fracty back bus bit ?
-P	WRITE	Negative True, Write/Read Type Cycle
1	·	l i
-R	ATN2	Negative True, CTU and Polled Interrupt
1	1	Request
!		!
-S		Not Used
-T	PRIOR IN	Bus Controller Priority In
-1	i EVIOV IN (Day Countrotter trioitry in
-U	PRIOR OUT	Bus Controller Priority Out
i		
-v	PROC ACTIVE	Negative True, Processor Active
1	j	(Controlling Bus)
-W	!	<u> </u>
		Not Used
-x	 	
-Y	REQ	Negative True, Request (Bus Data
-1	i ivera	Currently Valid)
•		
-z	ATN	Negative True, Data Comm Interrupt Request
222222222222		

Table 5.1 Connector Information

	Table 5.3	
Connector and Pin No.	Signal Name	Signal Description
P2, Pin 1 -2	BAO+ BAO-	
-4	BAI-	<pre> Data from the Terminals to a Modem or CPU (input)</pre>
-5 !		Daisy-Chain Signal Indicating that One of
-6 	1	Daisy-Chain Signal Originating at the Modem
-7 -8	CAI-	Daisy-Chain Signal Indicating that One of
-9 -10	CBI-	 Daisy-Chain Signal Originating at the Modem Indicating That it is Clear for the Termi- nal to Transmit (input)
-15 -11	BBO-	} Differential, Daisy-Chain Signal Carrying } Data from a Modem to Terminals in the Chain } (output)
-12 -13		} Not Used
-13	CE	RS232C Ring Indicator
1		

Table 5.1 Connector Information (Cont'd.)

3328257622225	=======================================	
Connector	Signal	Signal
and Pin No.		Description
======================================		
i	1	
P2, Pin A	GND	Frame Ground
1 -0		
-B	BA	RS232C Transmitted Data
i -c	ВВ	RS232C Received Data
1		
-D	I CA	RS232C Request To Send
i -E	i CB	RS232C Clear To Send
! _	1	!
-F	CC	RS232C Data Set Ready
-н	l AB	RS232C Signal Ground
1	1	l l
-J	CF.	RS232C Carrier Detect
-K	BBI+	 } Differential, Daisy-Chain Signal
-L	BBI-	Carrying Data From the Modem to the
1	1	Terminals (input)
i M	I SCA	 RS232C Secondary Channel Request To Send
1	1 SCA	Rozoze becomdary channer request to being
- N	SCF I	RS232C Secondary Channel Carrier Detect
1	1	DC222C Data Marminal Doads
-P	I CD (RS232C Data Terminal Ready
-R	CH (RS232C Data Signal Rate Selector
!	!	!
-s	∮ ▲	Not Used
i	1	
		·

Table 6.0 Module Bus Pin Assignments

:=====================================	1	i Bus
Performed: Output Data for Transmission	Value	Signal
•	======	=======================================
	i x	ADDR 15
Poll Bit: Switch Selectable (Bit 6-0)	I X	ADDR 14
	X	ADDR 13
Module Address: (ADDR 11,10,9,4) = (A11,A10,A9,A4)	i x	ADDR 12
Switch Selectable	A11	ADDR 11
Data Comm = (1110)	1 A10	ADDR 10
	1 A9	ADDR 9
Function Specifier: ADDR3 = 0	X	ADDR 8
ADDR5 = 1	l X	ADDR 7
ADDR6 = 1	1 1	ADDR 6
	1 1	ADDR 5
	I A4	I ADDR 4
	i 0	ADDR 3
Data Bus Bit Interpretation:	i x	ADDR 2
	i x	I ADDR 1
	i x	ADDR 0
B7	======	
Output Data Bit 7		BUS 7
desput bata bit /	1 B6	BUS 6
	1 85	BUS 5
86	1 B4	BUS 4
Output Data Bit 6	1 B3	BUS 3
Onthat para pre o	1 B2	BUS 2
	•	BUS 1
	•	
85		I BUS 0
Output Data Bit 5	•	
		al 1=Bus Low
n.a		al 0=Bus High
84	X=Don't	
Output Data Bit 4		
n.)		
B3		
Output Data Bit 3		
~ 0		
B2		
Output Data Bit 2		
B1		
Output Data Bit 1		
BO		
Output Data Bit O		
	=======	

Table 6.1 Module Bus Pin Assignments

Table 0,1 Module Bus Fin Assignment	, 	
Function	!	Bus
Performed: Output Control Byte 1	Value	· -
(Configuration Byte)	======	=======================================
1	I X	ADDR 15
Poll Bit: Switch Selectable (Bit 6-0)	l X	ADDR 14
1	l X	ADDR 13
Module Address: (ADDR 11,10,9,4) = (A11,A10,A9,A4)	Х	ADDR 12
Switch Selectable	A11	ADDR 11
Data Comm = (1110)	A10	ADDR 10
Data Comm - (1110)		
The state of the s	A 9	ADDR 9
Function Specifier: ADDR5 = 1	X	ADDR 8
ADDR6 = 0	l X	ADDR 7
1	1 0	ADDR 6
	1 1	ADDR 5
Data Bus Bit Interpretation:	i A4	ADDR 4
	X	ADDR 3
B7, B6 set number of data bits per character	X	ADDR 2
1	i X	ADDR 1
Data Bits	X	ADDR 1
Data Bits	! ^ !	
5 6 7 8	======	=======================================
	I B7	BUS 7
B7 0 0 1 1	I B6	BUS 6
	I B5	BUS 5
B6 0 1 0 1	B4	BUS 4
	l B3	BUS 3
i	l B2	BUS 2
I B5	B1	BUS 1
0 = Enable Parity	BO	BUS 0
	,	•
1 = Inhibit Parity		
		al 1=Bus Low
		al 0=Bus High
I B4	X=Don't	Care
0 = Odd Parity	=======	=======================================
1 = Even Parity		
1		
BAUD RATE		
1		
19200 50 75 110 134.5 150 300 600 200 1200 1800 2400	3600 490	10 7200 9600 I
1 =====================================		, /UVV
		1 1 1
B3 0 0 0 0 0 0 0 1 1 1 1 1	1 1	
B2 0 0 0 0 1 1 1 1 0 0 0	1 1	1 1 1
1		l
B1 0 0 1 1 0 0 1 1 0 0 1 1	0 (1 1 1
1 1		1
B0 0 1 0 1 0 1 0 1 0 1	0 1	1 0 1
	•	•

Table 6.2 Module Bus Pin Assignments

Function	!	l Bus
Performed: Output Control Byte 2	Value	•
(Modem Control Byte)	•	=========
	I X	ADDR 15
Poli Bit: Switch Selectable (Bit 6-0)	į X	ADDR 14
	I X	ADDR 13
Module Address: $(ADDR 11,10,9,4) = (A11,A10,A9,A4)$	I X	ADDR 12
Data Comm = (1110)	I A11	I ADDR 11
	I A10	ADDR 10
	1 A9	I ADDR 9
Function Specifier: ADDR5 = 0	X	I ADDR 8
ADDR6 = 1	l X	I ADDR 7
	1 1	ADDR 6
	1 0	I ADDR 5
Data Bus Bit Interpretation:	I A4	I ADDR 4
	I X	I ADDR 3
В7	l X	I ADDR 2
0 = Enable Daisy-Chain CAI and CBO	l X	ADDR 1
1 = Inhibit Daisy-Chain CAI and CBO	l X	ADDR 0
•	======	========
	i 87	i BUS 7
B6 Not Used	I B6	I BUS 6
De 1100 0000	I B5	BUS 5
	1 B4	BUS 4
B5 Not Used	1 B3	I BUS 3
Do Not osed	1 B2	BUS 2
	, B2	I BUS 1
B4	I B0	BUS 0
0 = Terminal Mode		
1 = Channel Monitor Mode	•	al 1=Bus Lo
1 - Channel Monitol Mode		al 0=Bus Hi
	X=Don't	
В3		
0 = CH On		
1 = CH Off		
n a		
B2		
0 = CD On		
1 = CD Off		
n4		
B1		
0 = SA On		
1 = SA Off		
В0		
0 = CA On		
1 = CA Off		

Table 6.3 Module Bus Pin Assignments

Table 6.3 Module Bus Pin Assignmen 	:CS :=========	
Function	1 (Bus
Performed: Output Control Bits	Value	Signal
	======	=========
	1 X (ADDR 15
Poll Bit: Switch Selectable (Bit 6-0)	1 X 1	ADDR 14
	1 X 1	ADDR 13
Module Address: $(ADDR 11,10,9,4) = (A11,A10,A9,A4)$	1 X 1	ADDR 12
Switch Selectable	A11	ADDR 11
Data Comm = (1110)	A10	ADDR 10
	1 A9 I	ADDR 9
Function Specifier: ADDR5 = 0	1 X 1	ADDR 8
ADDR6 = 0	1 A7 1	ADDR 7
	1 0 1	ADDR 6
Only one bit in each of the pairs AO,A1 and A3,A7	1 0 1	ADDR 5
Can be set to "1" for a given output command	1 A4 1	ADDR 4
	1 A3 (ADDR 3
AO	1 X I	ADDR 2
0 = No Effect	I A1 I	ADDR 1
1 = Reset Timer	1 AO 1	ADDR 0
	,	=======================================
A1	1 B7 1	BUS 7
0 = No Effect	B6	
1 = Set Timer	1 B5 1	BUS 5
	1 B4 1	BUS 4
A3	1 B3 1	BUS 3
0 = No Effect	B2	BUS 2
1 = Enable Transmission Complete Interrupt	B1	BUS 1
	B0	BUS 0
A7	•	=======================================
0 = No Effect		1 1=Bus Low
<pre>1 = Enable Transmitter Ready Interrupt</pre>		1 0=Bus Hig
	X=Don't	Care
	=======	=======================================
Data Bus Bit Interpretation: Not Applicable		
ndea Bus Bit Interpretation: Not Applicable		

Table 6.4 Module Bus Pin Assignments

======================================	 :	i Bus I
Function Performed: Input Received Character	 Value	· •
refrormed: input keceived character		· ·
		========
		ADDR 15
Poll Bit: Switch Selectable (Bit 6-0)	X	ADDR 14
	X	ADDR 13
Module Address: (ADDR 11,10,9,4) = (A11,A10,A9,A4)	X	ADDR 12
Switch Selectable	• ••••	ADDR 11
Data Comm = (1110)	A10	ADDR 10
	A9	ADDR 9 I
Function Specifier: ADDR5 = 0	Х	ADDR 8
ADDR6 = 0	X	ADDR 7
	0	ADDR 6
	i 0	ADDR 5
	I A4	ADDR 4
	X	ADDR 3
Data Bus Bit Interpretation:	X	ADDR 2
nara bus bit interpretation:		
	X	ADDR 1 I
	X	ADDR 0
В7	•	=======================================
Input Data Bit 7	1 B7	I BUS 7 I
	B6	BUS 6
	B5	BUS 5
B6	B4	BUS 4
Input Data Bit 6	B3	BUS 3
	В2	BUS 2
	B1	BUS 1
B5		BUS 0
Input Data Bit 5	•	
		al 1=Bus Low
		al 0=Bus Highi
	X=Don't	
Input Data Bit 4	=======	=======================================
		İ
		1
вз		1
Input Data Bit 3		ı
		i
		, i
82		
Input Data Bit 2		•
Input Data BIt 2		i
• •		
B1		
Input Data Bit 1		ı
		1
y .		1
80		i
Input Data Bit 0		i

Table 6.5 Module Bus Pin Assignments

Function	•	Bus
Performed: Input Status Byte 1	Value	
(Interrupt Status)	======	=======
	I X	ADDR 1
Poli Bit: Switch Selectable (Bit 6-0)	l X	ADDR 1
	l X	ADDR 1
Module Address: $(ADDR 11, 10, 9, 4) = (A11, A10, A9, A4)$	l X	ADDR 1
Switch Selectable	A11	ADDR 1
Data Comm = (1110)	A10	ADDR 1
	I A9	ADDR
Function Specifier: ADDR3 = 0	į X	ADDR
ADDR5 = 1	I X	ADD R
ADDR6 = 0	1 0	I ADDR
	1 1	ADDR
Data Bus Bit Interpretation: An interrupt is	I A4	ADDR
generated if any condition causes BO, B1, or	1 0	ADDR
B7 to be set to the "1" value. Values of B5	I X	I ADDR
and B6 are valid only if $B7 = 1$	I X	ADDR
	I X	ADDR
B7	======	=======
<pre>0 = Receiver Register Empty</pre>	1 B7	BUS 7
1 = Receiver Register Full (Cleared by	1 B6	BUS 6
inputting a character)	1 B5	BUS 5
, , , , , , , , , , , , , , , , , , , ,	I B4	BUS 4
Bó	I B3	BUS 3
0 = No Parity Error	1 B2	BUS 2
1 = Parity Error (Cleared by inputting status)	-	BUS 1
	I B0	BUS 0
B5		
0 = No Overrun Error	•	al 1=Bus L
1 = Overrun Error (Cleared by inputting		1 0=Bus H
status or character)	IX=Don't	
beauty of charactery		
B4, B3, B2 Not Used		
B1		
<pre>0 = No Timer Interrupt</pre>		
1 = Timer Interrupt Active (Cleared by resetting	the timer)
во		
0 = No Transmit Interrupt		
1 = Transmit Interrupt Active (Set when CB comes	un. cleare	ed by
outputting character or dropping CA or CB)	abl create	.u by
adebatering character of diobbing cy of CD)		

Table 6.6 Module Bus Pin Assignments

Function		l Bus I
Performed: Input Status Byte 2	Value	· ·
		-
(Modem Status)	•	========
	X	I ADDR 15 i
Poll Bit: Switch Selectable (Bit 6-0)	l X	I ADDR 14 I
	I X	ADDR 13
Modulo Addmoses (ADDD 41 40 0 4) = (A11 A10 A0 A4)	i x	
Module Address: $(ADDR 11, 10, 9, 4) = (A11, A10, A9, A4)$		ADDR 12
Switch Selectable	1 A11	I ADDR 11 I
Data Comm = (1110)	A10	I ADDR 10
	I A9	ADDR 9
Function Specifier: ADDR3 = 1	i X	ADDR 8
		-
ADDR5 = 1	į X	ADDR 7
ADDR6 = 0	1 0	I ADDR 6 I
	1 1	I ADDR 5 I
	1 A4	ADDR 4
Data Bus Bit Interpretation:	i i	ADDR 3
hard has bit intelhieration!	•	•
	i X	ADDR 2 I
B7	l X	ADDR 1
Always 1	l X	ADDR 0 I
Indicates Multipoint PCA in System	======	· · · · · · · · · · · · · · · · · · ·
indicator indicapolito for in System	1 B7	BUS 7
B6	1 B6	BUS 6
Always 0	I B5	I BUS 5 I
Indicates Async Multipoint PCA in System	1 B4	I BUS 4 I
•	1 B3	BUS 3
B5	1 B2	BUS 2
0 = CAI Off (Downline CA)	1 B1	BUS 1
1 = CAI On	1 B0	I BUS 0 I
	=======	
B4	li=Logic	al 1=Bus Low
0 = CE On		al 0=Bus Highi
1 = CE Off	X=Don't	care
	2222222	=======================================
B3		1
0 = SB On		i
		1
1 = SB Off		1
		1
B2		1
0 = CC On		i
1 = CC Off		1
1 - 00 011		!
		l
BI		I
0 = CF On		1
1 = CF Off		i
4 - UL UAL		
		!
D A		1
В0		•
0 = CB On		i
- 		İ

Table 6.7 Module Bus Pin Assignments

Function	1	l Bus I
Performed: Input Jumper Settings	Value	•
l	1======	========
i	X	ADDR 15
Poll Bit: Switch Selectable (Bit 6-0)	i â	ADDR 13
i Port Bit. Switch Selectable (Bit 0-0)	i x	ADDR 13
Module 34dmaggs (ADDD 44 40 0 4) = (344 340 30 34)	i x	·
Module Address: $(ADDR 11,10,9,4) = (A11,A10,A9,A4)$		ADDR 12
Switch Selectable	A11	ADDR 11
Data Comm = (1110)	A10	ADDR 10
	1 A9	ADDR 9 I
Function Specifier: ADDR5 = 0	I X	ADDR 8 I
ADDR6 = 1	l X	ADDR 7
	1 1	I ADDR 6 I
A3	1 0	ADDR 5
0 = Select Jumper Character 0 (J00-J07)	I A4	ADDR 4 I
1 = Select Jumper Character 1 (J10-J17)	1 A3	ADDR 3
•	ı X	ADDR 2
Data Bus Bit Interpretation:	i x	ADDR 1
	i x	ADDR 0
0 = Closed Switch	1======	=========
1 = Open Switch	1 B7	BUS 7
1 - Open bullen	B6	BUS 6
В7	I B5	BUS 5
	1 B4	BUS 4
Switch 7	•	•
	1 B3	BUS 3
B6	1 B2	BUS 2
switch 6	l B1	BUS 1
	1 B0	BUS 0
B5	,	=======================================
switch 5		al 1=Bus Low
		al O=Bus High!
B4	X=Don't	Care
Switch 4	========	
		1
вз		i
switch 3		ì
		i
B2		1
Switch 2		
Switch 2		†
B1		j
		į,
switch 1		1
2.0		
BO		1
switch 0		1
		1
	========	

FUNCTIONAL DESCRIPTION. Refer to the block diagram (figure 1), schematic diagram (figure 2), daisy-chain logical connection (figure 3), component location diagram (figure 4), and parts list (5061-2403, 5061-2409, 02640-60106, 02640-60132, 02640-60133, 02640-60134, 02640-60140, and 02645-60004) located in the appendix.

The purpose of the Asynchronous Multipoint Interface PCA is to transmit and receive start-stop serial data and provide modem control and status lines. Data character format is described in EIA RS404. The line interface is compatible with RS232C and the terminal's daisy-chain circuitry. Figure 3 illustrates the daisy-chain connection when more than one terminal is sharing an RS232C line. BA, BB, CA, and CB are RS232C signals; all others are daisy-chain signals.

- 3.1 UART AND DATA ROUTING.
- 3.1.1 The UART (Universal Asynchronous Receiver/Transmitter) is an LSI device used to convert from the 8-bit parallel data format of the terminal data bus to the serial, start-stop data format of the channel. The line monitor multiplexer determines whether data being transmitted from the modem or to the modem is sampled by the PCA.
- 3.1.2 The UART is a Western Digital TR1602B. The line monitor multiplexer (U58) is used to route received data to the UART. Normally it routes the BB (BBI) signal to the receiver input. However, if the channel monitor mode bit (Bit 4) is set in the control register and the CAI signal is high, the BAI signal will be routed to the UART receiver. This function is used when the terminal is operated as a passive line monitor.
- 3.2 BAUD RATE GENERATOR.
- 3.2.1 The baud rate generator uses the bus 4.915 MHz System Clock (SYS CLK) to generate a clock for the UART. The following rates are selectable programmatically: 50 (800 Hz), 75 (1200 Hz), 110 (1760 Hz), 134.5 (2152 Hz), 150 (2400 Hz), 200 (3200 Hz), 300 (4800 Hz), 600 (9600 Hz), 1200 (19.2 kHz), 1800 (28.8 kHz), 2400 (38.4 kHz), 3600 (57.6 kHz), 4800 (76.8 kHz), 7200 (115.2 kHz), 9600 (153.6 kHz), and 19200 (307.2 kHz).

- The first stage of the baud rate generator (U31 and U32) is a divide-by-5 and 1/3 circuit which divides the 4.915 MHz bus clock to 921.6 kHz for the MM5307 (U33). The circuit contains a synchronous counter and two J-K flip-flops (U32). The counter is programmed as a divide-by-5 or a divide-by-6 circuit depending on the state of the flip-flops. When the Q output of the first flip-flop (U32, Pin 9) is high, the counter divides by 5; when low it divides by 6. The counter divides by 5 twice and by 6 once to produce an average divisor of 5 and 1/3. The flip-flops (U32, Pin 9 and U32, Pin 5) follow the 11-00-10 sequence repetitively.
- 3.2.3 A 4-bit program constant is loaded into the LS175 register (U34) to program the circuit. (Refer to table 6.1 for additional information.) All of the baud rates except 200 and 19200 are generated by U33. When the 1000 code (200 baud) is in the baud rate register (U34) it is decoded by several gates and converted to a 1010 by an OR gate before reaching U33. This decoding logic also programs another synchronous counter (U25) to divide by 9. The 1010 code (1800 baud) on U33 provides an 1800 baud clock to the counter which divides it by 9 to generate the 200 baud clock. The proper routing of these signals is done with an LS51 AND-OR-Invert (U24) pack. When the generator is programmed with an 0000 code, the counter functions as a divide-by-16 while being clocked at 4.915 MHz. The divide-by-16 counter outputs 307.2 kHz (19200 baud) which is routed to the UART by the LS51 (U24).
- 3.3 INSTRUCTION DECODER.
- 3.3.1 The instruction decoder consists of an LS138 3-to-8 decoder (U47) and several gates. This circuit uses control and address lines on the terminal data bus to generate control signals on the PCA.

3.3.2 Four exclusive-OR gates (U46) that can be programmed by the A4, and A11 through A9 switches on the PCA are used to decode the module's address and to enable U47. The control signals are decoded as follows:

A D D R	A D D R	A D D R	W R I T	I /	R E	U 4	
6	5	3	E	0	Q	7	
-							
1	1	0	1	1	1	YO	Output data.
0	1	X	1	1	1	Y2	Output configuration byte.
1	0	X	1	1	1	Y 1	Output modem control byte.
0	0	Х	1	1	1	Y3	Output control bits.
0	1	0	0	1	1	Y6	Input interrupt status.
0	1	1	0	1	1		Input modem status.
1	0	0	0	1	1	Y5	Input Jumper 0 byte.
1	0	1	0	1	1		Input Jumper 1 byte.
0	0	X	0	1	1	¥7	Input data.

3.3.3 The control bits output command does several things depending on addresses 0, 1, 3, and 7:

```
A
    A
             A
D
    D
        D
             D
D
    D
        D
             D
R
    R
        R
             R
7
    3
        1
             0
X
                  Reset timer.
    X
        X
             1
X
                  Set timer.
    X
         1
             X
X
    1
         X
             X
                  Enable transmission complete interrupt.
    X
         X
             X
                  Enable transmitter ready interrupt.
```

3.3.4 Switch selectable interrupt polling is also provided. When a read operation is performed with POLL low, the PCA will drive one of the data bus lines (Bit0-6 switch selectable) low when an interrupt is active.

- 3.4 STATUS-JUMPER MULTIPLEXING.
- 3.4.1 This circuit is used to gate one of two status bytes (see tables 6.5 and 6.6) or one of two switch programmable bytes onto the terminal data bus. Four LS153 multiplexers (U22, U23, U13, and U12) are used for this function. The byte selection is as follows:

A E

0 0 Jumper 0

0 1 Jumper 1

1 0 Interrupt Status

1 1 Modem Status

- 3.5 CONTROL REGISTER. The control register is used to latch the RS232C outputs, control the line monitor multiplexer, and to inhibit or enable the daisy-chain control lines. Table 6.2 indicates the bit and polarity designation.
- 3.6 INTERRUPT LOGIC.
- 3.6.1 The interrupt network is primarily an AND-OR circuit. The AND function decides if an interrupt is active; the OR function directs the interrupt signal to the appropriate interrupt line. Interrupts can be

directed to the ATN or ATN2 line depending on the position of the INT

switch (open = ATN, closed = ATN2). There are three possible interrupts: transmit, receive, and timer. Whenever the UART receives a character it generates a Data Ready (DR) interrupt at U51, Pin 19. The Output Control Bits command can condition the transmit interrupt (U36, Pin 12) to be a Transmission Complete (U37, Pin 6) or a Transmitter Ready (U37, Pin 3) interrupt. The Transmission Complete interrupt indicates that both the Transmitter Register and the Transmitter Holding Register of the UART are empty. The Transmitter Ready interrupt indicates that the Transmitter Holding Register of the UART is empty. A transmit interrupt can only be active when both CA (CAO) and CB (CBI) are turned on. A timer interrupt occurs about 45 milliseconds after the timer is set. If the timer is reset within this period no interrupt will occur.

3.6.2 The timer network consists of a one-shot (U38), a flip-flop (U49, Pin 6), and a gate. When the timer is set, the Q output (Pin 8) of the one-shot goes high. At the next bus System Clock, the flip-flop is cleared (U49, Pin 6 goes high). When the one-shot times out (U38, Pin 6 goes high) a timer interrupt is generated (U39, Pin 8 goes high). This circuit can be reset at any time.

- 3.7 LINE RECEIVERS.
- Jine receivers are provided for both daisy-chain and RS232C operation. The RS232C receivers are standard MC1489A's (U18, U19) with a 330 pico-farad noise suppression capacitor. The daisy-chain data lines (BAI, BBI) are received with a high impedance (1.2K) Schottky (U410, U411) opto-isolator. These receivers are driven differentially. The daisy-chain control line receivers are high gain (4370) opto-isolators (U411, U511). The 470-ohm parallel input resistor is used to raise the threshold of the receiver to the midpoint of the received signal. All isolators are buffered with Schmitt inverters (U510).
- 3.8 LINE DRIVERS.
- Line drivers are provided for both RS232C and daisy-chain operation. The RS232C drivers are standard (U512, U29) with a 330 picofarad slew rate limiting capacitor. The daisy-chain data line drivers (BAO, BBO) are peripheral driver chips (U110, U210) with active pullup networks. Two 3.3-ohm resistors and an NPN transistor (Q2, Q4, Q6, Q8,) are used for temporary short circuit protection. The line is driven with a 17 volt differential signal. This scheme is used to make the drive signal symmetrical with respect to the threshold of the receiver. Symmetry is critical on the data signals. Delay is harmless. The control lines are driven with peripheral driver chips with a 330-ohm output impedance. The control lines are driven slowly to eliminate transmission effects.

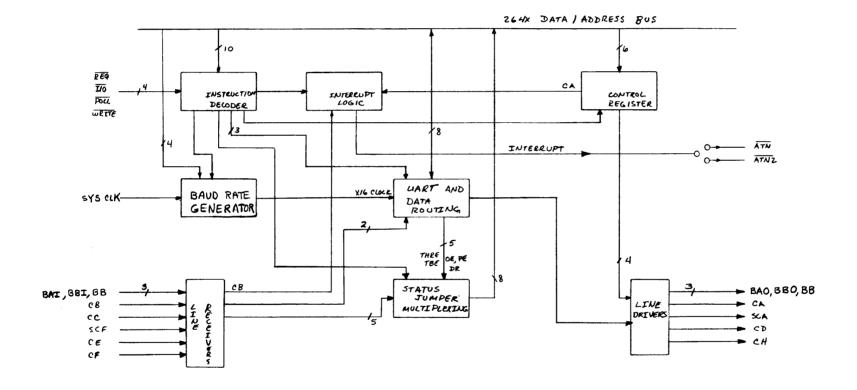


Figure 1
Asynchronous Multipoint Interface Block Diagram AUG-01-76
13255-91106

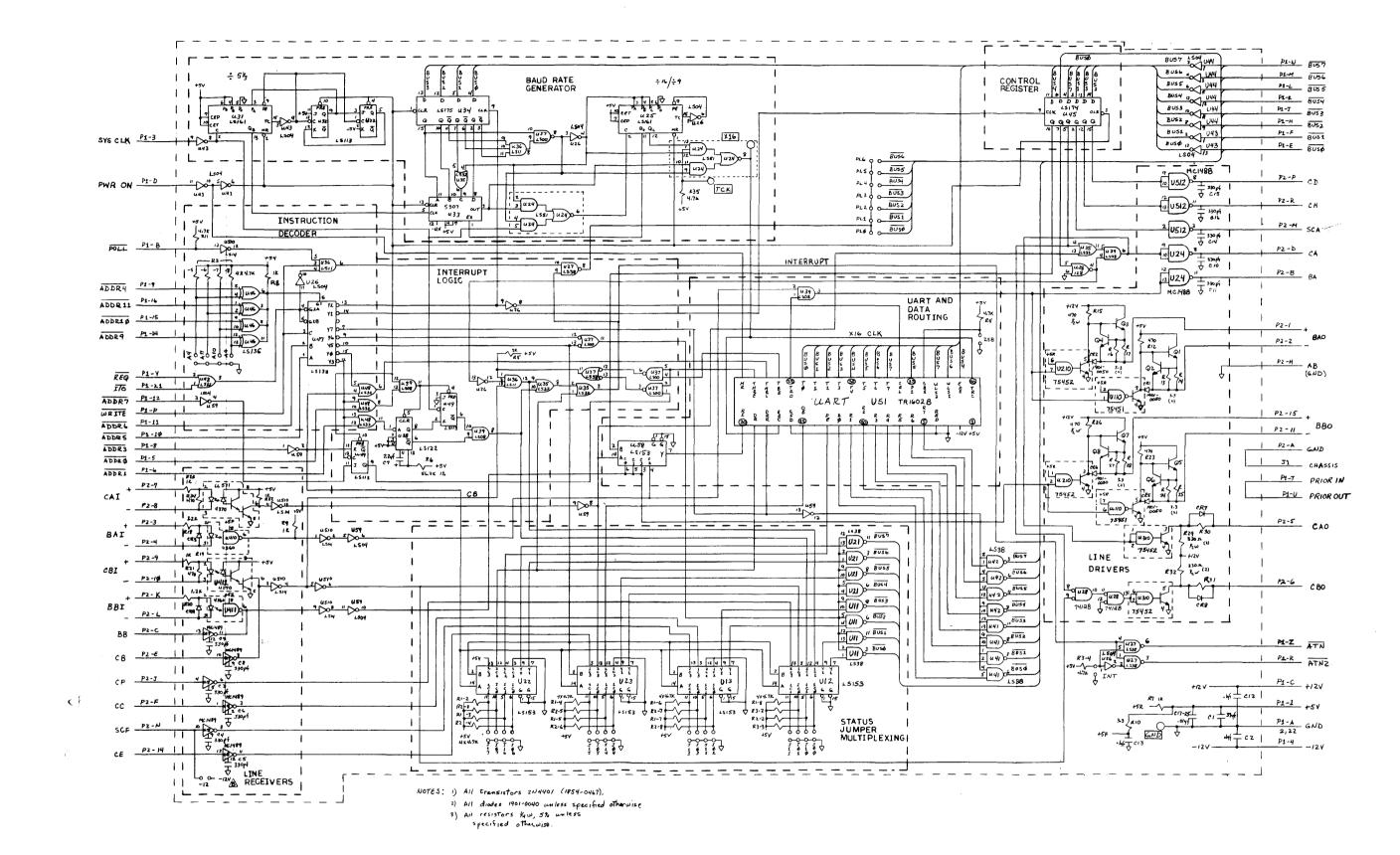


Figure 2
Asynchronous Multipoint Interface PCA
Schematic Diagram
AUG-01-76
13255-91106

TO OTHER To MODEM TERMINALS BA 🔫 - BAI The modern side signals BB can go directly to the modern BAOor through other terminals To the modem. BBI -► BBO DATA CONTROL CAI CBO CAO CBI -REQUEST CLEAR INHIBIT

WART

DATA

10

ZEND

SEND

DATA

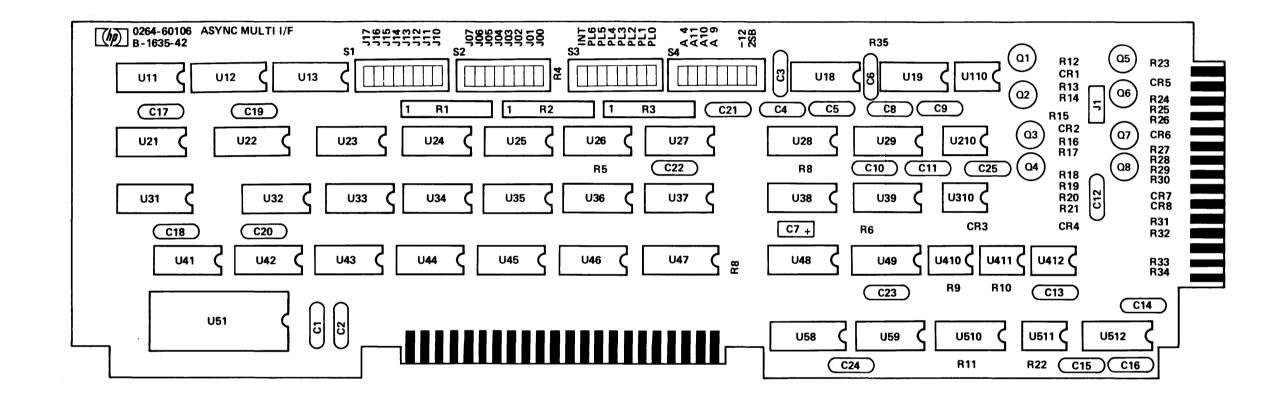
NoT€:

ASYNCHRONOUS MULTIPOINT

I/F PCA

DATSY

CHAIN



Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
	02440-60166	1	A SYNC MULTIPOINT INTERFACE ASSEMBLY DATE CODE: C-1635-42 REVISION DATE: 10-23-76	28 48 0	02640-69106
G1 G2 G3 G4 G5	01 60-0393 01 50-0121 01 60-3572 01 60-3572 01 60-3572	1 3 11	CAPACITOR-FXE 39UF+-10% 10VDC TA CAPACITOR-FXE -1UF +80-20% 50HVDC CER CAPACITOR-FXD 330PF +-10% 500HVDC CER CAPACITOR-FXD 330PF +-10% 500HVDC CER CAPACITOR-FXD 330PF +-10% 500HVDC CER	56289 28480 28480 28480 28480	1500396x9010B2 0150-0121 0160-3572 0160-3572 0160-3572
C6 C7 C8 C9 C10	0160-3572 0180-0197 0160-3572 0160-3572 0160-3572	1	CAPACITOR-FXD 330PF +-10% 500HVDC CER CAPACITOR-FXD 2-2UF+-10% 20VDC TA CAPACITOR-FXD 330PF +-10% 500HVDC CER CAPACITOR-FXD 330PF +-10% 500HVDC CER CAPACITOR-FXD 330PF +-10% 500HVDC CER	28480 56289 28480 28480 28480	0160-3572 1500225X9020A2 0160-3572 0160-3572 0160-3572
C11 C12 C13 C14 U15	0160-3572 0150-0121 0150-0121 0160-3572 0160-3572		CAPACITOR-FXD 330PF +-10% 500WVDC CER CAPACITOR-FXD -1UF +80-20% 50WVDC CER CAPACITOR-FXD -1UF +80-20% 500WVDC CER CAPACITOR-FXD 330PF +-10% 500WVDC CER CAPACITOR-FXD 330PF +-10% 500WVDC CER	28480 28480 28480 28480 28480	0160-3572 0150-0121 0150-0121 0160-3572 0160-3572
C16 C17 C18 C19 C20	0160-3572 0160-2055 0160-2055 0160-2055 0160-2055	9	CAPACITOR-FXB 330PF +-10% 500WVDC CER CAPACITOR-FXD .01UF >80-20% 100WVDC CER CAPACITOR-FXD .01UF +80-20% 100WVDC CER CAPACITOR-FXD .01UF >80-20% 100WVDC CER CAPACITOR-FXD .01UF +80-20% 100WVDC CER	28480 28480 28480 28480 28480	0160-3572 0160-2055 0160-2055 0160-2055 0160-2055
C21 C22 C23 C24 C25	0160-2055 0160-2055 0160-2055 0160-2055 0160-2055		CAPACITOR-FXD .01UF +80-20% 100WVDC CER CAPACITOR-FXD .01UF +80-20% 100WVDC CER CAPACITOR-FXD .01UF +80-20% 100WVDC CER CAPACITOR-FXD .01UF +80-20% 100WVDC CER CAPACITOR-FXD .01UF +80-20% 100WVDC CER	28480 28480 28480 28480 28480	0160-2055 0160-2055 0160-2055 0160-2055 0160-2055
CR1 GR2 GR3 GR4 GR5	19C1-0050 19C1-0050 19C1-0040 19C1-0040 19C1-0050	4	DIODE-SWITCHING 80V 200MA 2NS DD-7 DIODE-SWITCHING 80V 200MA 2NS DO-7 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 30V 50MA 2NS DO-35 DIODE-SWITCHING 80V 200MA 2NS DO-7	28480 28480 28480 28480 28480	1901-0050 1901-0050 1901-0040 1901-0040 1901-0050
CR6 CR7 CR8	19C1-0050 19C1-0040 19C1-0040		DIODE-SWITCHING BOV 200MA 2MS DO-7 DIODE-SWITCHING 30V 50MA 2MS DO-35 DIODE-SWITCHING 30V 50MA 2MS DO-35	28480 28480 28480	1901-0050 1901-0040 1901-0040
£1 £2 £3	0360-0124 0360-0124 0360-0124	3	TERMINAL-STUC SGL-PIN PRESS-MTG TERMINAL-STUD SGL-PIN PRESS-MTG TERMINAL-STUD SGL-PIN PRESS-MTG	28480 28480 28480	0360-0124 0360-0124 0360-0124
11	1251-1126	1	CONNECTOR-SGL CONT SKT .08-IN-8SC-SZ RND	74970	105-0754-001
u1 92 93 94 95	1854-0467 1854-0467 1854-0467 1854-0467 1854-0467	8	TRANSISTOR NPN 2N4401 SI TO-92 PD=310MW TRANSISTOR NPN 2N4401 SI TO-92 PD=310MW TRANSISTOR NBN 2N4401 SI TO-92 PD=310MW TRANSISTOR NBN 2N4401 SI TO-92 PD=310MW TRANSISTOR NBN 2N4401 SI TO-92 PD=310MW	04713 04713 04713 04713 04713	2N4401 2N4401 2N4401 2N4401 2N4401
u6 u7 u8	1854-0467 1854-0467 1854-0467		TRANSISTOR NAN 204401 SI TO-92 PD=310MW TRANSISTOR NAN 204401 SI TO-92 PD=310MW TRANSISTOR NAN 204401 SI TO-92 PD=310MW	04713 04713 04713	2N4401 2N4401 2N4401
R1 K2 K3 R6 R5	1810-0125 1810-0125 1810-0125 06 63-4725 06 63-1025	3 3 7	METMORK-RES 8-PIN-SIP .125-PIN-SPCG NETMORK-RES 8-PIN-SIP .125-PIN-SPCG NETMORK-RES 8-PIN-SIP .125-PIN-SPCG RESISTOR 4.7K 5% .25W FC TC=-400/+700 RESISTOR 1K 5% .25W FC TC=-400/+600	11236 11236 11236 01121 01121	750 750 750 750 CB4725 CB1025
K6 R7 K8 K9 K10	0757-0459 0683-1025 0683-1025 0683-1025 0683-0335	1	RESISTOR 56.2K 1% .125M F TC=0+100 RESISTOR 1K 5% .25M FC TC=-400/+600 RESISTOR 1K 5% .25M FC TC=-400/+600 RESISTOR 1K 5% .25M FC TC=-400/+600 RESISTOR 3.3 5% .25M FC TC=-400/+500	24546 01121 01121 01121 01121	C4-1/8-T0-5622-F CB1025 CB1025 CB1025 CB3365
R11 R12 R13 R14 R15	06 E3-4725 06 E3-4715 06 E3-0335 06 E3-0335 06 86-4715	4	RESISTOR 4.7K 5% .25M FC TC=-400/+700 RESISTOR 470 5% .25M FC TC=-400/+600 RESISTOR 3.3 5% .25M FC TC=-400/+500 RESISTOR 3.3 5% .25M FC TC=-400/+500 RESISTOR 470 5% .5M FC TC=0+529	01121 01121 01121 01121 01121	C84725 C84715 C83365 C83365 E84715
R16 R17 R18 R19 R20	0643-0335 0643-0335 0643-1225 0643-1025 0643-1225	2	RESISTOR 3.3 5% .25W FC TC=-400/+500 RESISTOR 3.3 5% .25W FC TC=-400/+500 RESISTOR 1.2K 5% .25W FC TC=-400/+700 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 1.2K 5% .25W FC TC=-400/+700	01121 01121 01121 01121 01121	CB3365 CB3365 CB1225 CB1025 CB1025 CB1225
R21 R22 R23 R24 R25	06 & 2 - 4715 06 & 2 - 1025 06 & 2 - 4715 06 & 2 - 0335 06 & 2 - 0335		RESISTOR 470 5% .25M FC TC=-400/+600 RESISTOR 1K 5% .25M FC TC=-400/+600 RESISTOR 470 5% .25M FC TC=-400/+600 RESISTOR 3.3 5% .25M FC TC=-400/+500 RESISTOR 3.3 5% .25M FC TC=-400/+500	01121 01121 01121 01121 01121	CB4715 CB1025 CB4715 CB33G5 CB33G5

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
NZO NZ7 NZ8 NZ9 NZ9	06 66-4715 06 63-0335 06 63-0335 06 66-3315 06 66-3315	4	ASYNC MULTIPOINT INTERFACE ASSEMBLY CONT'D. RESISTOR 470 5% .5M CC TC=0+529 RESISTOR 3.3 5% .25M FC TC=-400/+500 RESISTOR 3.3 5% .25M FC TC=-400/+500 RESISTOR 330 5% .5M CC TC=0+529 RESISTOR 330 5% .5M CC TC=0+529	01121 01121 01121 01121 01121	E84715 C833G5 C833G5 E83315 E83315
R31 R32 R33 R34 R35	06 86-3315 06 86-3315 06 83-1025 06 83-4715 06 83-4725		RESISTOR 330 5% .5W CC TC=0+529 RESISTOR 330 5% .5W CC TC=0+529 RESISTOR 1K 5% .25W FC TC=-400/+600 RESISTOR 470 5% .25W FC TC=-400/+600 RESISTOR 4.7K 5% .25W FC TC=-400/+700	01121 01121 01121 01121 01121	E83315 E83315 C81025 C84715 C84725
\$1 \$2 \$3	31 C1-2094 31 31-0392 31 C1-2094 31 31-0392 31 C1-2094 31 31-0392	4	SWITCH-TGL DIP ROCKER ASSEMBLY 8-1A MS COV-RKR 0.922 IN LG; 0.422 IN M; 0.217 SWITCH-TGL DIP ROCKER ASSEMBLY 8-1A MS COV-RKR 0.922 IN LG; 0.422 IN M; 0.217 SWITCH-TGL DIP ROCKER ASSEMBLY 8-1A MS COV-RKR 0.922 IN LG; 0.422 IN M; 0.217	28480 28480 28480 28480 28480 28480	3101-2094 3131-0392 3101-2094 3131-0392 3101-2094 3131-0392
54	31 C1-2094 31 31-0392		SWITCH-TGL DIP ROCKER ASSEMBLY 8-1A NS COV-RKR 0.922 IN LG; 0.422 IN W; 0.217	28480 28480	3101-209 4 3131-0392
U11 U12 U15 U18 U19	1820-1209 1820-1244 1820-1244 1820-0990 1820-0990	5 5 2	IC-DIGITAL SN74LS38N TTL LS QUAD 2 NAND IC-DIGITAL SN74LS153N TTL LS 4 IC-DIGITAL SN74LS153N TTL LS 4 IC-DIGITAL MC1489AL DTL QUAD NAND IC-DIGITAL MC1489AL DTL QUAD NAND	01295 01295 01295 01295 04713	SN74L S38N SN74L S153N SN74L S153N NC1489AL MC1489AL
UZ1 UZ2 UZ3 UZ4 UZ5	1820-1209 1840-1244 1820-1244 1820-1210 1820-1818	1 2	IC-DIGITAL SN74LS38N TTL LS QUAD 2 NAND IC-DIGITAL SN74LS153N TTL LS 4 IC-DIGITAL SN74LS153N TTL LS 4 IC-DIGITAL SN74LS51N TTL LS DUAL 2 IC, DIGITAL SN74LS51N TTL LS DUAL 2	01295 01295 01295 01295 28480	SN74L S3 8N SN74L S1 53N SN74L S1 53N SN74L S5 1N 1820–1818
U26 U27 U28 U29 U31	1820-1199 1820-1209 1820-1074 1820-0509 1820-1418	4 1 2	IC-DIGITAL SN74LSO4N TTL LS HEX 1 IC-DIGITAL SN74LS38N TTL LS QUAD 2 NAND IC-DIGITAL SN74128N TTL QUAD 2 NOR IC-DIGITAL MC1488L DTL QUAD LINE IC, DIGITAL 74LS161	01295 01295 01295 01295 04713 28480	SN74LS04N SN74LS38N SN74128N MC1488L 1820-1818
U32 U33 U34 U35 U36	1820-1213 1820-1348 1820-1195 1820-1208 1820-1203	2 1 1 2 1	IC-DIGITAL SN74LS113N TTL LS DUAL IC-DIGITAL NN5307 PMGS IC-DIGITAL SN74LS175N TTL LS QUAD IC-DIGITAL SN74LS32N TTL LS QUAD 2 OR IC-DIGITAL SN74LS31N TTL LS TPL 3 AND	01295 27014 01295 01295 01295	SN74L S113N MM5307N SN74L S175N SN74L S32N SN74L S1EN
U37 U38 U39 U41 U42	1820-1197 1820-1422 1820-1201 1820-1209 1820-1209	1 1 1	IC-DIGITAL SN74LSOON TTL LS QUAD 2 NAND IC-DIGITAL SN74LS122N TTL LS IC-DIGITAL SN74LS0GN TTL LS QUAD 2 AND IC-DIGITAL SN74LS38N TTL LS QUAD 2 NAND IC-DIGITAL SN74LS38N TTL LS QUAD 2 NAND	01295 01295 01295 01295 01295	SN74L SOON SN74L SL22N SN74L SOBN SN74L S3BN SN74L S3BN
U43 U44 U45 U46 U47	1820-1199 1820-1199 1820-1196 1820-1215 1820-1216	1 1 1	IC-DIGITAL SA74LSO4N TTL LS HEX 1 IC-DIGITAL SA74LSO4N TTL LS HEX 1 IC-DIGITAL SA74LS174N TTL LS HEX IC-DIGITAL SA74LS174N TTL LS QUAD 2 IC-DIGITAL SA74LS138N TTL LS QUAD 2	01295 01295 01295 01295 01295	SN74L SO4N SN74L SO4N SN74L S1 74N SN74L S1 36N SN74L S1 38N
U48 U49 U51 U56 U59	1820-1208 1820-1213 1820-1219 1820-1244 1820-1199	1	IC-DIGITAL SN74LS32N TTL LS QUAD 2 OR IC-DIGITAL SN74LS113N TTL LS DUAL IC-DIGITAL TR1602B TTL* IC-DIGITAL SN74LS153N TTL LS 4 IC-DIGITAL SN74LS04N TTL LS HEX 1	01295 01295 0026H 01295 01295	SN74L S32N SN74L S113N TR1602B SN74L S153N SN74L S04N
U110 U210 U310 U410 U411	1820-0535 1820-0799 1820-0799 1950-0429 1950-0429	1 2 2	IC-DIGITAL SN75451BP TTL DUAL 2 AND IC-DIGITAL SN75452BP TTL DUAL 2 NAND IC-DIGITAL SN75452BP TTL DUAL 2 NAND OPTO-ISOLATOR LED-IC GATE IF-10MA-MAX OPTO-ISOLATOR LED-IC GATE IF-10MA-MAX	01295 01295 01295 28480 28480	SN754518P SN754528P SN754528P 1990-0429
U412 U51U U511 U512	1950-0494 1820-1416 1950-0494 1820-0509	2	OPTO-ISOLATOR LED-PDIO/XSTR IF=20MA-MAX IC-DIGITAL SN74LS14N TTL LS HEX 1 INV OPTO-ISOLATOR LED-PDIO/XSTR IF=20MA-MAX IC-DIGITAL MC1488L DTL QUAD LINE	28480 01295 28480 04713	1990-0494 SN74LS14N 1990-0494 MC1488L

Poference	Replaceable Paris						
Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number		
	02640-60134	1	CABLE EXTENDER ASSEMBLY REVISION DATE: 10-23-76	28480	02640-60134		
	0850-0855 1251-0142 1251-0352 1251-3271 8120-2305	1 2 1	TUBING-HS .046-D/.023-RCVO .016-WALL CONNECTOR 14-PIN M MICRO RIBBON CABLE BUSHING-CIRC STD CONN CONNECTOR 14-PIN F MICRORIBBON CBL SHIELDED	92194 90949 71468 90949 28480	F1T-221-3/64 CLEAR 57-30140 CA18220-6 57-60140 8120-2305		

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
	02640-60132	1	CABLE ASSEMBLY	28480	02640-60132
	0624-0098 0850-0855 1251-0159	2	REVISION DATE: 09-04-76 SCREM-TPG 4-40 .438-IN-LG PAN-HD-POZI TUBING-HS .046-D/.023-RCVD .016-MALL CONNECTOR-PC EDGE 15-CONT/ROW 2-ROWS	28480 92194 71785	0624-0099 FIT-221-3/64 CLEAR 251-15-30-261
	0890-0291 1251-2417	1	TBG HS BLK .375D CONNECTOR 25-PIN M D SERIES CONTACT-CONN MALE CRP .04-IN-CONI-SZ	71468 71468	DBC-25P-F0 030-1952-002
	1251-3253 1251-3271 1251-3320 1251-3328 1251-4339	16 1 2 2 1	CONNECTOR 14-PIN F MICRORIBBON ACCESSORY-SUBMIN D CONN ACCESSORY-SUBMIN D CONN POLARIZING KEY-PC EDGE CONN	9D949 71468 71785 28480	57-60140 018-5000-167 423-42-22-022 1251-4339
	2150-0078 2200-0149 2200-0757 2220-0010 2260-0001	2 1 2 2 2	MASHER-LX HLCL NO4 .115-IN-ID SCREW-MACH 4-40 .625-IN-LG PAN-HD-POZI SCREW-MACH 4-40 .688-IN-LG PAN-HD-POZI SCREW-MACH 4-40 .5-IN-LG FIL-HL-SLT NUT-HEX-DBL-GHAM 4-40-THD .094-THK	28480 28480 28480 28480 28480	2190-0078 2200-0149 2200-0757 2200-0010 2260-0082
	2260-0002 3030-0009 8120-1930 8120-2305 5040-6003	1	NUT-HEX-DBL-CHAM 4-40-THD .062-THK SCREW-SET 6-32 .375-IN-LG SMALL CUP-PT CABLE-UNSHLD 26AMG 18-CNDCT JGK-JKT CABLE-SHLD 22AMG 8-CNDCT BLK-JKI .27-DD CLAMP	28480 28480 28480 28480 28480	2260-0005 3030-0009 8120-1930 8120-2305 5040-6003
	5040-6072 5040-6086	1	MOUNTING BLOCK HOOD, CONNECTOR	28480 28480	5040-6072 5040-6086
				:	

Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
	03445-40004		CELE TEST CONNECTOR ACCOUNTY	22.00	
	02645-60004 0360-0174 0470-0251	2	SELF TEST CGMNECTOR ASSEMBLY REVISION DATE: 09-10-76 STANDOFF-RND .25LG 4-40THD .18CD BRS ADHESIVE-SEALANT	28480 88245	02645-60004 1550A-11
	0850-0855 1251-2416 1251-3252	1 15	TUBING-HS .046-D/.023-RCVD .016-WALL CONNECTOR 25-PIN F D SERIES CONTACT-CONN FEM CRP .04-IN-CONT-SZ	3G003 92194 71468 71468	CLEAR SEAL F1T-221-3/64 CLEAR DBC-255-F0 030-1953-000
	1251-3320 1251-3328 19C1-0040 2150-0078 22C0-0757	1 2 2 2 2	ACCESSORY-SUBMIN D CONN ACCESSORY-SUBMIN D CONN DIODE-SWITCHING 30V 50MA 2NS DO-35 WASHER-LK HLCL NO4 .115-IN-ID SCREN-MACH 4-40 .688-IN-LG PAN-HD-POZI	71468 71785 28480 28480 28480	018-5000-167 423-42-22-022 1901-0040 2190-0078 2200-0757
	2220-0010 2260-0001 8150-2344	2 2 1	SCREM-MACH 4-40 .5-IN-LG FIL-HD-SLT NUT-HEX-DBL-CHAM 4-40-THD .094-THK WIRE 24AMG 8K 300V PVC 7X32 80C	28480 28480 28480	2229-0010 2260-0002 8150-2344
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	Replacedole Farts						
Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number		
	5061-2404	1	EUROPEAN MODEM CABLE ASSEMBLY REVISION DATE: 06-23-76	28480	5061-2404		
	0624-0098 1251-0159 1251-0352 1251-2417 1251-3253	2 1 1 1	SCREM-TPG 4-40 .438-IN-LG PAN-HD-PDZI CUNNECTOR-PC EDGE 15-CONT/ROW 2-ROWS CABLE BUSHING-CIRC STD CONN CONNECTOR 25-PIN M D SENIES CONTACT-CONN MALE CRP .04-IN-CONT-SZ	28480 71785 71468 71468 71468	0624-0099 251-15-30-261 CA18220-6 DBC-25P-F0 030-1952-002		
	1251-3320 1251-3328 1251-4339 2150-0078 2200-0091	1 2 1 2 1	ACCESSORY-SUBMIN D CONN ACCESSORY-SUBMIN D CONN POLARIZING KEY-PC EDGE CONN MASHER-LK HLCL NO4 - 115-1N-ID SCREW-MACH 4-40 -562-IN-LG PAN-HD-POZI	71468 71785 28480 28480 28480	018-5000-167 423-42-22-022 1251-4339 2190-0078 2200-0091		
	2200-0757 2220-0010 2260-0001 2260-0002 3030-0143	2 2 2 1 1	SCREM-MACH 4-40 .688-IN-LG PAN-HD-POZI SCREM-MACH 4-40 .5-IN-LG FIL-HD-SLT NUT-HEX-DBL-CHAM 4-40-THD .094-THK NUT-HEX-DBL-CHAM 4-40-THD .062-THK SCREM-SET 6-32 .5-IN-LG SMALL CUP-PT ALY	28480 28480 28480 28480 28480	2200-0757 2220-0010 2260-0002 2260-0005 3030-0143		
i	8120-1930 5040-6003 5040-6072 5040-6086]]]	CABLE—UNSHLD 26ANG 18—CNDCT JGK—JKT CLAMP MUUNTING BLOCK HOOD, CONNECTOR	28480 28480 28480 28480	8120-1930 5040-6003 5040-6072 5040-6086		
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Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
	02640-60133	1	MULTIPGINT CABLE ASSEMBLY	28480	02640-60133
	0624-0098 1251-0142 1251-0159 1251-0352 1251-3271	2 1 1 2	REVISION DATE: 09-04-76 SCREM-TPG 4-40 .438-IN-LG PAN-HD-POZI CONNECTOR 14-PIN M MICRO RIBBON CONNECTOR-PC EDGE 15-CONT/ROW 2-ROWS CABLE BUSHING-CIRC STD CONN CONNECTOR 14-PIN F MICRORIBBON	28480 90949 71785 71468 90949	0624-0099 57-30140 251-15-30-261 CA18220-6 57-60140
	1251-4339 2200-0091 2240-0002 3030-0143 8120-2305	2 1 1 1	POLARIZING KEY-PC EDGE CONN SCREW-MACH 4-40 .562-IN-LG PAN-HD-POZI NUT-HEX-DBL-CHAM 4-40-THD .062-THK SCREW-SET 6-32 .5-IN-LG SMALL CUP-PT ALY CABLE-SHLD 22AWG 8-CNDCT BLK-JKT .27-00	28480 28480 28480 28480 28480	1251-4339 2200-0091 2260-0005 3030-0143 8120-2305
	50 40-6004 50 40-6072 50 40-6086 0890-0291 0890-0855	1 1 1	CLAMP, CABLE, SMALL MOUNTING BLOCK MOOD, CONNECTOR TBG HS BLK .375D TBG HS CLR .046D	28480 28480 28480	5040-6004 5040-6072 5040-6086
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Reference Designation	HP Part Number	Qty	Description	Mfr Code	Mfr Part Number
	5061-2403	1	MODEM BYPASS CABLE ASSEMBLY REVISION DATE: 09-10-76	28480	5061-2403
	03 60-0174 1251-0352 1251-2416 1251-3251 1251-3252	4 2 18 10	STANDOFF-RND .25LG 4-40THD .18CD BRS CABLE BUSHING-CIRC STD CONN CONNECTOR 25-PIN F D SERIES CONTACT-CONN FEM CRP .04-IN-CONT-SZ CONTACT-CONN FEM CRP .04-IN-CONT-SZ	88245 71468 71468 71468 71468	1550A-11 CA18220-6 OBC-25S-F0 030-1953-002 030-1953-000
	1251-3320 1251-3328 2150-0078 2200-0757 2220-0010	4 4 4 4	ACCESSORY-SUBMIN D CONN ACCESSORY-SUBMIN D CONN WASHER-LK HLCL NO4 .115-IN-ID SCREW-MACH 4-40 .688-IN-LG PAN-HD-POZI SCREW-MACH 4-40 .5-IN-LG FIL-HD-SLT	71468 71785 28480 28480 28480	018-5000-167 423-42-22-022 2190-0078 2200-0757 2220-0010
	2260-0001 8150-2344	4	NUT-HEX-DBL-CHAM 4-40-THD .094-THK WIRE 24AWG BK 300V PVC 7X32 80C	28480 28480	2260-0002 8150-23 44
	0890-0291 8120-1930		TBG HS BLK .375D CA 18 X 26 GA UL		
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